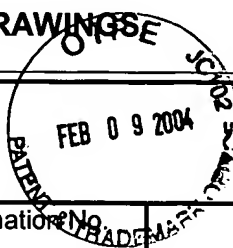


TRANSMITTAL OF FORMAL DRAWINGSDocket No.
YOR920030533US1 (17110)

In Re Application Of: Steven J. Koester



Serial No.

10/717,279

Filing Date

November 19, 2003

Confirmation No.

Unknown

Examiner

Unknown

Art Unit

Unknown

Invention: **METHOD OF REDUCING DISLOCATION-INDUCED LEAKAGE IN A STRAINED-LAYER FIELD-EFFECT TRANSISTOR**

Address to:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

10 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

Steven Fischman
Registration No. 34,594Correspondence Address
Customer No. 23389

Dated: February 5, 2005

I certify that this document and attached formal drawings are being deposited on February 5, 2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

Steven Fischman

Typed or Printed Name of Person Mailing Correspondence